UP04599

Silicon NPN epitaxial planar type

For high-frequency amplification (Tr1) For low-frequency amplification (Tr2)

■ Features

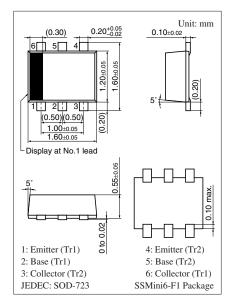
- Two elements incorporated into one package (Each transistor is separated)
- Reduction of the mounting area and assembly cost by one half

■ Basic Part Number

• 2SC3354 + 2SC3311A

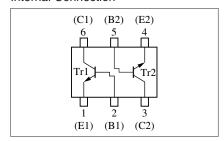
■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter		Symbol	Rating	Unit	
Tr1	Collector-base voltage	V_{CBO}	30	V	
	(Emitter open)				
	Collector-emitter voltage	V _{CEO}	20	V	
	(Base open)				
	Emitter-base voltage	V_{EBO}	3	V	
	(Collector open)				
	Collector current	I_C	50	mA	
Tr2	Collector-base voltage	V _{CBO}	60	V	
	(Emitter open)				
	Collector-emitter voltage	V _{CEO}	50	V	
	(Base open)				
	Emitter-base voltage	V _{EBO}	7	V	
	(Collector open)				
	Collector current	I_C	100	mA	
	Peak collector current	I_{CP}	200	mA	
Overall	Total power dissipation	P _T	125	mW	
	Junction temperature	T _j	125	°C	
	Storage temperature	T _{stg}	-55 to +125	°C	



Marking Symbol: 3U

Internal Connection



\blacksquare Electrical Characteristics $T_a = 25 ^{\circ}C \pm 3 ^{\circ}C$

• Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 100 \mu\text{A}, I_E = 0$	30			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = 10 \ \mu A, I_C = 0$	3			V
Forward current transfer ratio	h_{FE}	$V_{CB} = 10 \text{ V}, I_{E} = -2 \text{ mA}$	25		250	_
Base-emitter voltage	V _{BE}	$V_{CB} = 10 \text{ V}, I_{E} = -2 \text{ mA}$		720		mV
Reverse transfer capacitance (Common base)	C _{rb}	$V_{CE} = 6 \text{ V}, I_{C} = 0, f = 1 \text{ MHz}$		0.8		pF
Power gain	PG	$V_{CB} = 10 \text{ V}, I_E = -1 \text{ mA}, f = 200 \text{ MHz}$		20		dB
Reverse transfer capacitance (Common emitter)	C _{re}	$V_{CB} = 10 \text{ V}, I_E = -1 \text{ mA}, f = 10.7 \text{ MHz}$		1.0	1.5	pF
Transition frequency *	f_T	$V_{CB} = 10 \text{ V}, I_E = -15 \text{ mA}, f = 200 \text{ MHz}$	800	1 300	1600	MHz

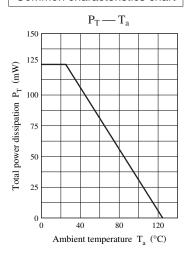
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Pulse measurement

• Tr2

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 2 \text{ mA}, I_B = 0$	50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = 10 \ \mu A, I_C = 0$	7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 20 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 10 \text{ V}, I_{B} = 0$			100	μΑ
Forward current transfer ratio	h _{FE1}	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$	160		460	_
	h _{FE2} *	$V_{CE} = 2 \text{ V}, I_{C} = 100 \text{ mA}$	90			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$		0.1	0.3	V
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_{E} = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		3.5		pF
(Common base, input open circuited)						

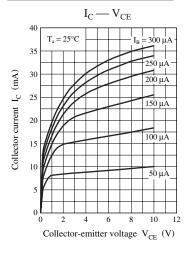
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Pulse measurement

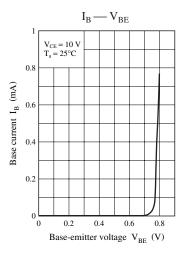
Common characteristics chart

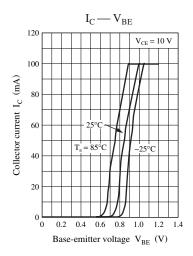


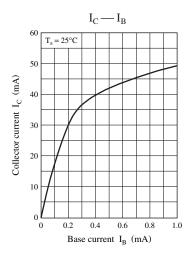
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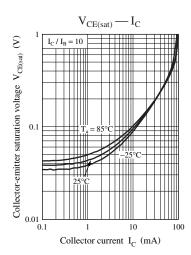
Characteristics charts of Tr1

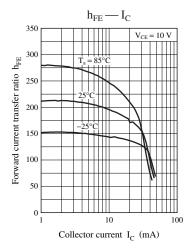


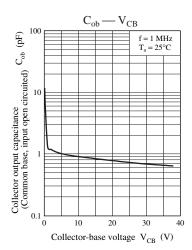








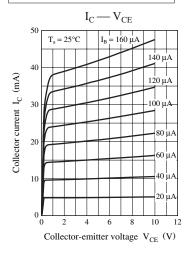


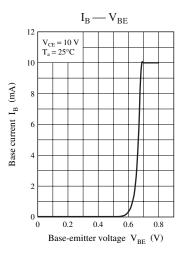


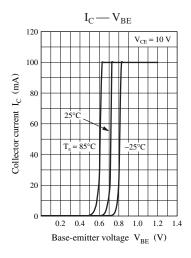
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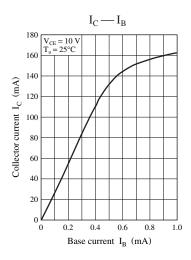
Panasonic

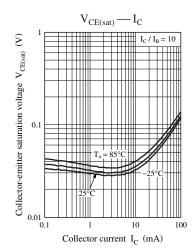
Characteristics charts of Tr2

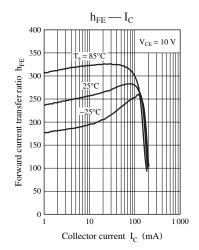


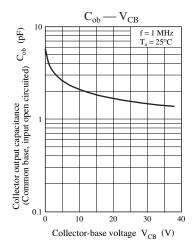












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